
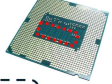
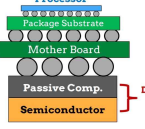
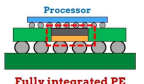
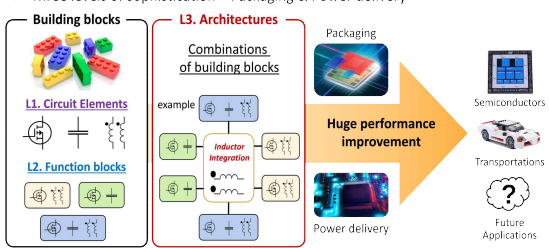
	<p>■ <b>Contact information</b></p> <p>Professor : E3-2 5224 E-mail : jaeil.baek@kaist.ac.kr          Lab. : E3-2 5234 E-mail : yunsu.k@kaist.ac.kr          Website : <a href="https://ipc.kaist.ac.kr">https://ipc.kaist.ac.kr</a></p>
<p>■ <b>Current state of the Lab. (in 2025 Spring Semester)</b></p> <p>Master's Student: 2 Undergraduate student : 7</p>	
<p>■ <b>Research Areas: High Performance Integrated Power Electronics</b></p> <div style="display: flex; justify-content: space-around; align-items: flex-start;"> <div style="text-align: center;"> <p><b>Discrete Power Electronics</b></p>  <p>Large volume</p> </div> <div style="text-align: center;"> <p><b>Integrated Power Electronics</b></p>  <p>High power capability Small volume</p> </div> <div style="text-align: center;"> <p><b>Current Vertical Delivery</b></p>  <p>Discrete PE</p> </div> <div style="text-align: center;"> <p><b>Future Vertical Delivery</b></p>  <p>Fully integrated PE (Power chip)</p> </div> </div> <p style="text-align: center; margin-top: 10px;">Same Power</p> <p style="text-align: center; margin-top: 10px;">Emerging Requirements</p> <p>Our research group is focused on innovations for high performance integrated power electronics. Traditional discrete power electronics can provide high power capability but limited by its large volume. On the other hand, PMIC and integrated power electronics are very small but limited by its low power capability. The main research area of our group is about how to develop smarter, greener, smaller, and higher-power integrated power electronics (or power chips) for emerging and future applications. The following are some of the key research areas we are primarily interested in:</p> <div style="display: flex; align-items: flex-start;"> <div style="flex: 1;"> <p>➢ Three levels of sophistication + Packaging &amp; Power delivery</p> <ul style="list-style-type: none"> <li><input type="checkbox"/> <b>Building Block</b> <ul style="list-style-type: none"> <li>✓ Circuit element: Magnetics</li> <li>✓ Function blocks: Circuit &amp; Control</li> </ul> </li> <li><input type="checkbox"/> <b>Architecture</b> <ul style="list-style-type: none"> <li>✓ Innovative combinations of building blocks</li> </ul> </li> <li><input type="checkbox"/> <b>Packaging &amp; Power Delivery</b></li> <li><input type="checkbox"/> <b>Emerging &amp; Future Applications</b> <ul style="list-style-type: none"> <li>✓ AI Semiconductors, Robot, Transportation, etc.</li> </ul> </li> </ul> </div> <div style="flex: 2; margin-left: 20px;">  </div> </div>	
<p>■ <b>Recommended courses &amp; Career after graduation</b></p> <ul style="list-style-type: none"> <li>• <b>Recommended Courses</b> Circuit theory, Electronics circuits, Power electronics</li> <li>• <b>Career after graduation</b> Academia, Industry, National lab</li> </ul>	<p>■ <b>Introduction to other activities besides research</b></p> <p>We hold seasonal workshops in both summer and winter. Our members actively participate in various sports such as running and soccer. We maintain a warm, respectful, and collaborative lab environment.</p>
<p>■ <b>Introduction to the Lab.</b></p> <p><u>We are looking for self-motivated students!</u> Are you interested in power and energy crisis we are facing these days? Would you like to do lab activities more than just computer simulation? We are waiting for you!</p>	
<p>■ <b>Recent research achievements ('22~'24)</b></p> <p>[1] Vertical Stacked LEGO-PoL CPU Voltage Regulator, <b>IEEE TPEL 2022 (First Prize Paper)</b>.</p> <p>[2] MIPS: Multiphase Integrated Planar Symmetric Coupled Inductor for Ultrathin VRM, <b>IEEE TPEL 2023</b>.</p> <p>[3] CoaxMIL 2.0 – Next Generation Coaxial Magnetic Integrated Inductors for Higher Efficiency Fully Integrated Voltage Regulator, <b>IEEE ECTC 2024</b>.</p>	