
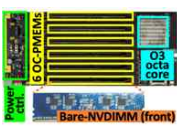


	Computer Architecture and Memory Systems Laboratory	Contact information <table><tr><td>Professor</td><td>Email: m.jung@kaist.ac.kr</td><td>Tel: 042-350-7455</td></tr><tr><td>Lab.</td><td>Email: steelyk7@kaist.ac.kr</td><td>Tel: 042-350-7555</td></tr><tr><td>Website</td><td colspan="2">https://camelab.org</td></tr></table>		Professor	Email: m.jung@kaist.ac.kr	Tel: 042-350-7455	Lab.	Email: steelyk7@kaist.ac.kr	Tel: 042-350-7555	Website	https://camelab.org	
Professor	Email: m.jung@kaist.ac.kr	Tel: 042-350-7455										
Lab.	Email: steelyk7@kaist.ac.kr	Tel: 042-350-7555										
Website	https://camelab.org											
■ Research Areas												
► CXL Hardware and Software co-solution  <ul style="list-style-type: none">Opening a new direction for memory disaggregationEnsuring direct accessible and high-performance capabilities		► Kernel & Storage Architecture  <ul style="list-style-type: none">High performance SSD architectures and firmware designIn-memory processing and In-storage processing										
► Machine Learning with Storage/SCM  <ul style="list-style-type: none">Exploring ML algorithms to make system-related decisionsImplementing hardware acceleration architectures using ML within Memory and storage		► Next Gen. Non-Volatile Memory (NVM)  <ul style="list-style-type: none">Overcoming challenges of emerging NVMs such as RRAM and PRAMArchitecting new platforms with byte-addressable NVMs										
► Heterogeneous Computing  <ul style="list-style-type: none">Researching energy-efficient heterogeneous computing with diverse devicesRemove data movement by aggressively integrating memory with hardware accelerator		► New Memory Computing  <ul style="list-style-type: none">New memory device design and implementation (e.g. Z-NAND, PRAM)Exploring a new territory to integrate new memory into domain specific accelerator										
■ Recommended courses & Career after graduation We recommend taking courses related to operating systems (OS), system programming, computer architecture, machine learning and field programmable gate array (FPGA) . It would be better to have experiences with simulators or benchmark tools. Though all those courses and experiences listed above aren't mandatory. About career, based on your will, Dr. Jung will support everything for you to get publications and to become a leading researcher at from industry to faculty jobs.												
■ Introduction to other activities besides research We regard horizontal and active communications as important. So, we often have mealtimes and talking time together. Now, we are moving forward together encouraging each other. In addition, we sometimes visit abroad to attend top-tier academic conferences. If you're interested, check out our lab's instagram. :) @camelab_members												
■ Introduction to the Lab. Professor Jung has advised his students at UT Dallas, Yonsei Univ. and now KAIST under support and collaborations with U.S. government organizations, industries (Intel, Western Digital, Sandisk, Samsung, SK Hynix, Memray) and institutions (UIUC, Georgia tech). Our lab have published many papers to top-tier conferences and gotten attention in many presses. We continue to target top-tier conference publications in a perfect environment for research.												
■ Recent research achievements (2012 - 2025) <ul style="list-style-type: none">46 publications in top-tier conferences. (Total 136 publications including major conferences and SCI journals.)Our system research is ranked first in Korea, according to the metrics-based system, CSRankings.15 international articles, 181 domestic articles including Korea major presses and Naver news headline.96 international and domestic patents.												